

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	14	(monitor\$4 same execution\$4 same program\$4) and (debug\$4 same logic) and micro adj controlle\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:16
L2	14	(monitor\$4 same execution\$4 same program\$4) and (debug\$4 same logic) and micro adj controlle\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:17
L3	4	caus\$4 same debug\$4 same logic same exception same memory same address	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:17
L4	3	717/124.ccls. and (address same range) and debug and (break same point) and trigg\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:17
L5	7	717/124.ccls. and (address same range) and debug and (break same point)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:18
L6	1	(monitor\$ same program\$4 same execution\$4) and (debug with logic) near5 (micro adj control\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:18
L7	1	monitor\$4 same executio\$4 same debug adj logic and monitor\$4 and (initializ\$4 same program\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 14:19

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L8	7	717/124.ccls. and (address same range) and debug and (break same point)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 15:06
L9	14	(monitor\$4 same execution\$4 same program\$4) and (debug\$4 same logic) and micro adj controlle\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 15:06
L10	84	717/127-129.ccls. and (monitor\$4 same execution\$4 same program\$4) and (debug\$4 same logic)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:02
L11	129	(initializ\$4 same program\$4) same (micro adj control\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:02
L12	70	(debug same logic\$4) and excep\$4 and (illegal same (address and range))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:03
L13	300	(debug same logic\$4) and excep\$4 and address and range and illegal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:03
L14	0	717/124.ccls. and (illegal same access same storage same area)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:04

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L15	38	execut\$5 near5 (swap\$4 or chang\$4 same code same flash same RAM) and (micro adj controller)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:43
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